

Abstract

A low cross talk compensation circuit comprising a first signal pair having a first conductor and a second conductor parallel to the first conductor, and a second signal pair having a third conductor and a fourth conductor parallel to the third conductor, wherein
5 each conductor is attached to a corresponding input signal. A first compensation line attached to the first input signal and a second compensation line attached to the third input signal are intertwined forming a first compensation line assembly with capacitive and inductive coupling parallel to and flanked by the second and third conductors. A third
10 compensation line attached to the fourth input signal and a fourth compensation line attached to the second input signal are intertwined forming a second compensation line assembly with capacitive and inductive coupling parallel to and adjacent to the second conductor.